



## SUPPLEMENTAL APPLICATION DATA SHEET

### APPLICATION INFORMATION

Application Number:: 10/532,105  
Application Date:: 10/12/05  
Application Type:: **REGULAR**  
Subject Matter:: **UTILITY**  
CD-ROM or CD-R?:: **NONE**  
Title:: **METHOD AND DEVICE FOR  
SYNTHESISING AN ELECTRICAL  
ARCHITECTURE**  
Attorney Docket Number:: 271243US2X PCT  
Total Drawing Sheets:: 20

### INVENTOR INFORMATION

Applicant Authority Type:: **INVENTOR**  
Primary Citizenship Country:: **France**  
Status:: **FULL CAPACITY**  
Given Name:: **Samuel**  
Family Name:: **BOUTIN**  
City of Residence:: **Magny les Hameaux**  
Country of Residence:: **France**  
Street of Mailing Address:: **10, chemin de la Chapelle**  
City of Mailing Address:: **Magny les Hameaux**  
Country of Mailing Address:: **France**  
Postal or Zip Code of Mailing Address:: **78114**

Applicant Authority Type:: **INVENTOR**  
Primary Citizenship Country:: **France**  
Status:: **FULL CAPACITY**  
Given Name:: **Christophe**  
Family Name:: **DANG VAN NHAN**  
City of Residence:: **Villejuif**  
Country of Residence:: **France**  
Street of Mailing Address:: **10 Allee de la Capitainerie**  
City of Mailing Address:: **Villejuif**  
Country of Mailing Address:: **France**  
Postal or Zip Code of Mailing Address:: **94800**

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**CORRESPONDENCE INFORMATION**

Correspondence Customer Number:: 22850

**REPRESENTATIVE INFORMATION**

Representative Customer Number:: 22850

**DOMESTIC PRIORITY INFORMATION**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	National Stage of	PCT/FR03/03107	10/21/03

**FOREIGN PRIORITY INFORMATION**

Application Number:	Country::	Filing Date::	Priority Claimed::
02/13112	France	10/21/02	YES

**ASSIGNMENT INFORMATION**

Assignee Name:: RENAULT S.A.S.  
Street of Mailing Address:: 13-15 quai Alphonse Le Gallo  
City of Mailing Address:: Boulogne Billancourt  
Country of Mailing Address:: France  
Postal or Zip Code of Mailing Address:: 92100

0580



DOCKET NO.: 271243US2XPCT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

RE APPLICATION OF:

Samuel BOUTIN, et al.

GROUP: 2876

SERIAL NO: 10/532,105

FILED: October 12, 2005

EXAMINER:

FOR: METHOD AND DEVICE FOR SYNTHESISING AN ELECTRICAL  
ARCHITECTURE

**SUBMISSION OF SUPPLEMENTAL APPLICATION DATA SHEET**

Office of Initial Patent Examination  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant(s) submit herewith a Supplemental Application Data Sheet for the above-identified application for the purpose of correcting the 2<sup>nd</sup> applicant's name.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

*Raymond F. Cardillo Jr.*

Marvin J. Spivak  
Registration No. 24,913  
Raymond F. Cardillo, Jr.  
Registration No. 40,440

Customer Number

22850

Tel. (703) 413-3000  
Fax. (703) 413-2220  
(OSMMN 06/04)

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